

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Antonio Maria Borneo et al.

Application No. : 10/612,831 Filed : July 1, 2003

For : PROCESS FOR RUNNING PROGRAMS ON PROCESSORS AND

CORRESPONDING PROCESSOR SYSTEM

Art Unit : 2183

Docket No. : 851763.435

Date : October 28, 2003

Mail Stop Missing Parts Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references are enclosed).

Applicants wish to disclose and have officially considered the following copending related application by the same assignee: Application No. 10/612,825, filed July 1, 2003, entitled PROCESS FOR RUNNING PROGRAMS WITH SELECTABLE INSTRUCTION LENGTH PROCESSORS AND CORRESPONDING PROCESSOR SYSTEM.

As to any reference supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Supplemental Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicants believe this Supplemental Information Disclosure Statement has been timely filed, however, the Commissioner is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,

Antonio Maria Borneo et al.

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Enclosures:

Form PTO-1449 Cited References (3)

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Sheet 1 of 1

Date: 10/28/03

FORM PTO-1449 (REV.7-80) OCT 2 8 2003

.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE ATTY. DOCKET NO. APPLICATION NO. 851763.435 10/612,831 APPLICANTS

Antonio Maria Borneo et al. INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) GROUP ART UNIT FILING DATE July 1, 2003 2183 **U.S. PATENT DOCUMENTS** *EXAMINER FILING DATE CLASS **SUBCLASS** DOCUMENT NUMBER DATE NAME INITIAL IF APPROPRIATE AA AB ACAD ΑE AF AG AH ΑI **FOREIGN PATENT DOCUMENTS** TRANSLATION DOCUMENT NUMBER DATE COUNTRY YES 0 924 603 A2 06/23/99 EP ΑK 1 102 166 A2 05/23/01 EP ΑL AM AN ΑO OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.) Biglari-Abhari, M. et al., "Improving Binary Compatibility in VLIW Machines Through ΑP Compiler Assisted Dynamic Rescheduling," in Proceedings of the Euromicro Conference, Euromicro 26, vol. 1, Los Alamitos, CA, September 5-7, 2000, pp. 386-393. AQ **EXAMINER** DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).